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THIRD SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Complete if Known	
				Application Number	09/788,670
				Filing Date	February 21, 2001
				First Named Inventor	Hartvig W.J. EKNER
				Art Unit	2193
				Examiner Name	Do, Chat C.
Sheet	1	of	1	Attorney Docket Number	1778.2110000

NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume issue number(s), publisher, city and/or country where published	T ²	
CD	NPL1	Drescher, W. and Fettweis, G., "VLSI Architectures for Multiplication in $GF(2^m)$ for Application Tailored Digital Signal Processors," in <i>VLSI Signal Processing, IX</i> , San Francisco, California, USA (October 30 - November 1, 1996), IEEE, Piscataway, New Jersey, USA, pp. 55-64.		
CD	NPL2	Drescher, W. <i>et al.</i> , "VLSI Architecture for Datapath Integration of Arithmetic Over $GF(2^m)$ on Digital Signal Processors," in <i>IEEE International Conference on Acoustics, Speech, and Signal Processing</i> , Munich, Germany (April 21-24, 1997), IEEE, Los Alamitos, California, USA, pp. 631-634.		
CD	NPL3	Texas Instruments Application Report, "The MSP430 Hardware Multiplier, Functions and Applications," [online] April 1999, [Retrieved on February 16, 2006] Retrieved from the Internet: <URL: http://focus.ti.com/lit/an/slaa042/sla042.pdf > (34 pages).		
CD	NPL4	INTERSIL Data Sheet, "HS-RTX2010RH: Radiation Hardened Real Time Express Microcontroller," [online] March 2000, [Retrieved on February 16, 2006] Retrieved from the Internet: <URL: http://www.intersil.com/data/fn/fn3961.pdf > (36 pages).		
	NPL5			
	NPL6			
	NPL7			
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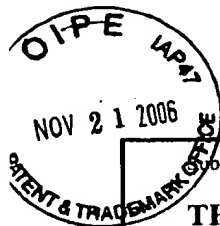
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U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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CD	FP1	DE 196 44 688 A1	04-30-1998	Technische Universitat Dresden		
	FP2					
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